



#9/2
V. Pham
BOX AF
REPLY UNDER
37 C.F.R. § 1.116
EXPEDITED PROCEDURE

PATENT
2332-0117P
FEB - 6 2003
RECEIVED
TECHNOLOGY CENTER 2800

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: KIM, Jeong Soo, et al. Conf. No.: 4564
Appl. No.: 09/998,134 Group: 2823
Filed: December 3, 2001 Examiner: Thanh V. PHAM
For: METHOD OF FORMING A JUNCTION IN SEMICONDUCTOR
DEVICE USING HALO IMPLANT PROCESSING

AMENDMENT

*2002-01-03
TJP
3/3/03*
BOX AF

Honorable Commissioner of Patents
Washington, D.C. 20231

February 3, 2003

Sir:

In response to the Examiner's Final Office Action dated October 2, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS

Please amend the claims as follows:

1. (Twice Amended) A method for forming a junction in a semiconductor device, comprising the steps of: